COPPERHEAD™ HIGH SPEED DUAL TRANSFORMERS

Ruggedized



 Compliant with ANSI X3T111, Fiber Channel, FC-PH-3 for quarter/full speed applications, SMPTE, IEEE-1394 FireWire

Dielectric Withstanding Voltage (DWV): 1500 Vrms

• Operating & Storage Temperature: -55° C to $+125^{\circ}$ C

• Lead Finish: Sn63/Pb37

• Moisture Sensitivity Level: 3

Militi

Electrical Specifications @ 25°C									
Part Number	Turns Ratio (±5%)	Primary Inductance @1Vrms, 100kHz (µH MIN)	DCR MAX (Ω)	Application Nominal Bit Rate (Mbaud)					
T-330SCT	1СТ:1СТ	26	0.20	265.6 (1/4 speed)					
T-531SCT	1CT:1CT	7.5	0.20	531 (half speed)					
T-1062SCT	1CT:1CT	3.75	0.20	1062.5 (full speed)					
T-1250SCT	1CT:1CT	3.75	0.20	1250					
T-1485SCT	1СТ:1СТ	3.75	0.20	1485 (SMPTE)					

NOTES:

1. For Tape & Reel packaging, add "T" suffix at the end of the part number: i.e. T-330SCTT

Electrical Schematics Mechanicals Dimensions: inch [mm] Tolerance (unless otherwise specified): ±0.010 [0.254] T-330SCT, T-531SCT, T-1062SCT, T-1250SCT and T-1485SCT 10 **-**O 16 0.510 [12.95] MAX O 14 3 C 0.300 [7.62] MAX 13 0.420±0.015 [10.67±0.38] 0.307 0.475 12 **O**11 HHHO 10 0.020 [0.51] 12X 0.030 [0.76] 0.050 [1.27] 0.050 [1.27] **TOP VIEW** 0.350 [8.89] PCB PAD PATTERN (REFERENCE ONLY) 0.180 [4.57] MAX 0.005 [0.13] FRONT VIEW SIDE VIEW



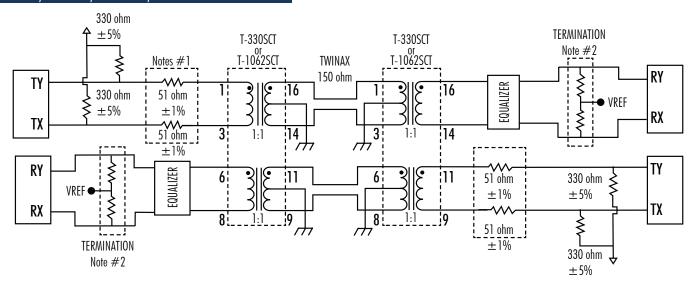
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Typical Application Circuit

T-330SCT, T-531SCT, T-1062SCT, T-1250SCT and T-1485SCT



APPLICATION NOTE:

- 1. The transformer, 51Ω resistors, and the impedance of the driver are matched to achieve the best return loss (S11) for the transmitter of the 150Ω system.
- 2. The total impedance of the termination resistor network is 150Ω .
- 3. When laying out PCB, transmission line methods must be utilized to maintain return loss and signal integrity. Transformer must be located within 0.50" of the DB9 connector.
- 4. It is recommended that the center tap (CT) of the transformer(s), cable side, be connected to earth/chassis (cable shield) ground either directly or via a transient voltage suppressor (TVS) type compoennt and earth/chassis ground should be "AC-coupled" to signal (digital) ground through a 0.27μF, 500ν capacitor.

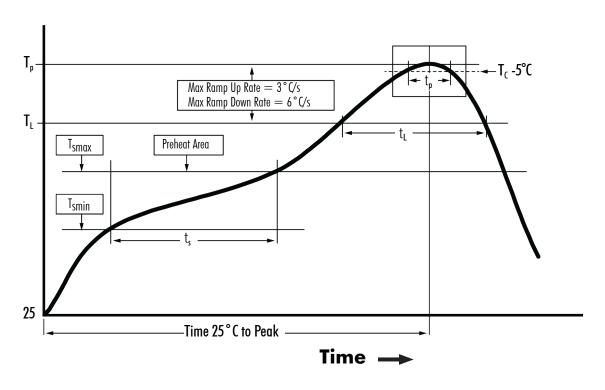


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Recommended Reflow Profile (Based on J-STD-020D)



T _{smin} (°C)	T _{smax} (°C)	T _ւ (°C)	T _p (°C MAX)	† _s (s)	† _L (s)	t _P (s MAX)	Ramp-up rate (T _L to T _P)	Ramp-down rate (T _P to T _L)	Time 25°C to peak temperature (s MAX)
Tin/Lead Profile									
100	150	183	235	60 - 120	60 - 150	20	3°C/s MAX	6°C/s MAX	360

NOTES:

- 1. All temperatures measured on the package leads.
- 2. Maximum number of reflow cycles not to exceed 2.



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