HIGH ISOLATION GATE DRIVE TRANSFORMER

Ruggedized

PL3215NL

- Rugged design for industrial application
- Operating Frequency: 50kHz to 300kHz
- Lead Finish:Pure Tin

Electrical Specifications @ 25 °C – Operating Temperature – 40 °C to +125 °C

Part Number	Turns Ratio 100kHz 0.1Vrms (±2%)	ET (V * µsec MAX)	Primary Inductance 100kHz 0.1Vrms (µH MIN) (1-10)	Leakage Inductance Gate to Drive (µH MAX)	DCR Drive (1-10) (mΩ ±20%)	DCR Gates (mΩ ±20%)	"Hi-Pot 60Hz 60s (Vrms)" Drive- Gate	"Hi-Pot 60Hz 60s (Vrms)" Gate-Gate
PL3215NL	1:1:1	115	686	0.8	710	710	6000	4000

Notes: 1. Inductance and leakage inductance are measured with Agilent 4284A or equivalent.

2. Turns ratio is measured with Wayne Kerr 3260B or equivalent.
3. DCR is measured with Valhalla Scientific 4150ATC or equivalent.
4. Hi-Pot is performed with Kikusui TOS5051 or equivalent.
5. The peak flux density should remain below 2100 Gauss to ensure that the core does not saturate. Use the following procedure acl culate the peak flux density:
   - A. Calculate the Volt-µsec product (ET):
     ET = 10 * (Drive Voltage) * (Don) / (Frequency in kHz)
   - B. Calculate the operating flux density (B): BPK (Gauss) = X * ET/Ff where:
     Ff = 1 for unipolar drive applications and 2 for bipolar drive applications,

Electrical Schematic

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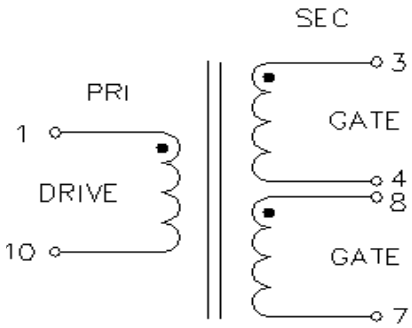
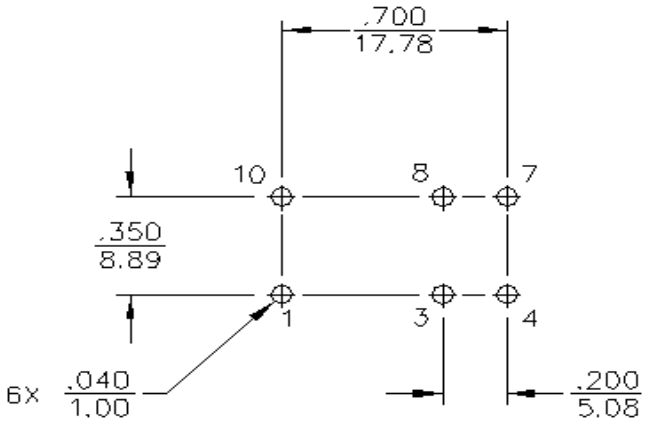


Suggested Land Pattern



Mechanical

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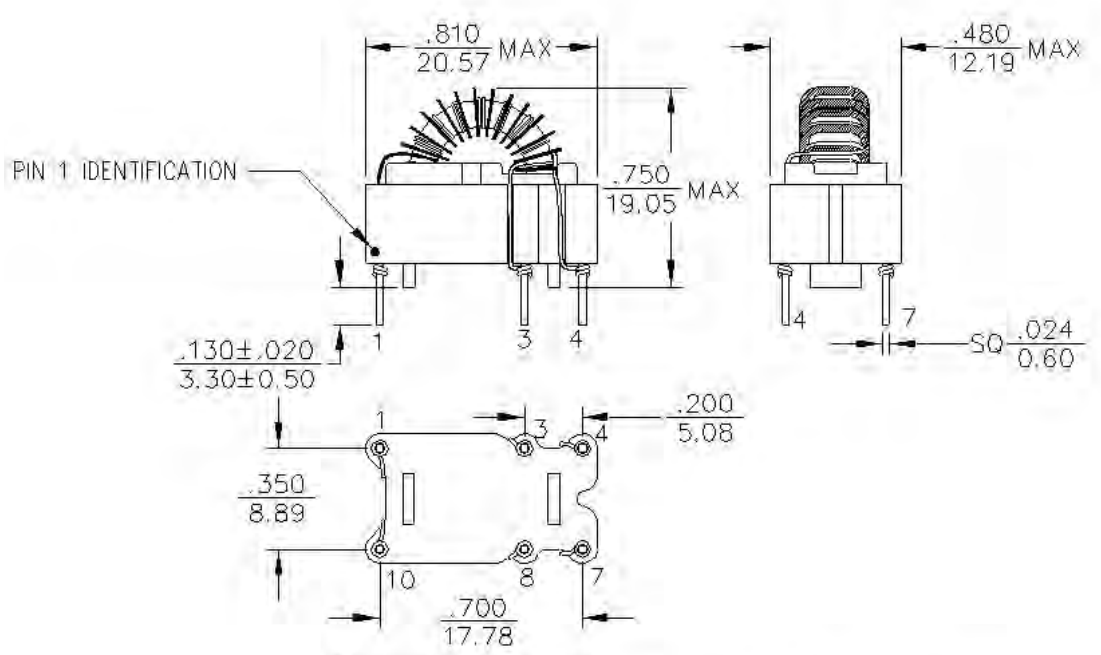

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