

GATE DRIVE TRANSFORMER

Ruggedized



- ⚙️ 1500Vrms Continuous isolation between windings
- ⚙️ Moisture Sensitivity Level: 1
- ⚙️ Lead Finish: Pure Tin.
- ⚙️ 4000Vrms gate to drive isolation

Electrical Specifications @ 25 °C – Operating Temperature – 55 °C to +130 °C

Part Number	Turns Ratio 100kHz 1Vrms (±2%)		ET (V * μsec) MAX	Inductance 100kHz, 100mVrms (mH MIN)	Leakage Inductance 100kHz, 100mVrms (μH MAX)	Parasitic Capacitance 100kHz 100mVrms (pF MAX)	DC Resistance (Ω MAX)			Dielectric Withstand Voltage 6S MIN (Vrms)	
	(1-4):(8-7)	(1-4):(6-5)					(1-4)	(8-7)	(6-5)	(1,2,4)to (5,6,7,8)	(5,6)to (7,8)
PL3602NL	2.0	2.0	375	5.40	8.0	60	2.2	1.6	1.6	4000	1500

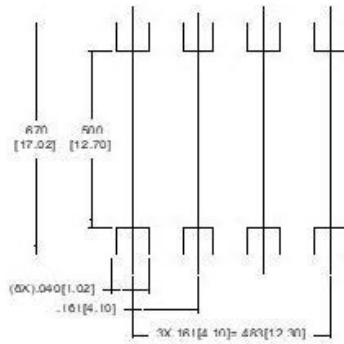
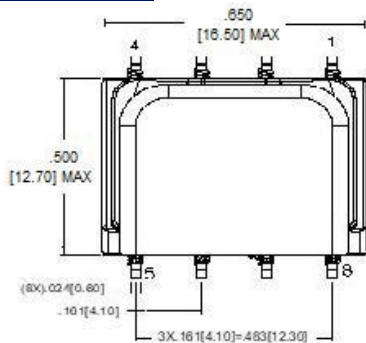
Notes: 1. The Maximum volt-μsec to limit the core loss and temperature at 100 kHz base on a bipolar flux swing of 2100Ga peak.

2. Optional Tape & Reel packaging can be ordered by adding a "T" suffix to the part number (i.e. **PL3602NLT**).

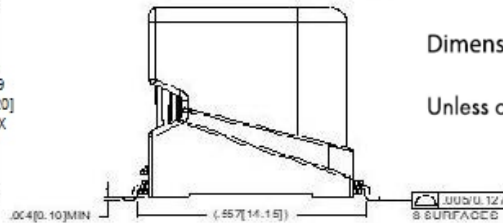
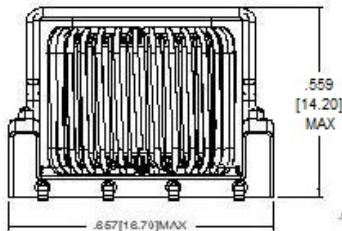
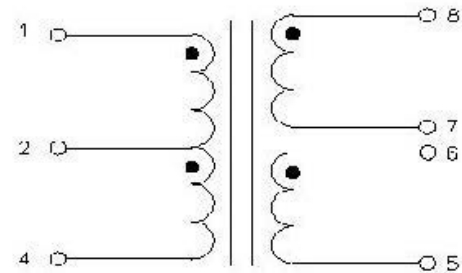
Mechanical

Electrical Schematic

PL3602NL



SUGGESTED PAD LAYOUT

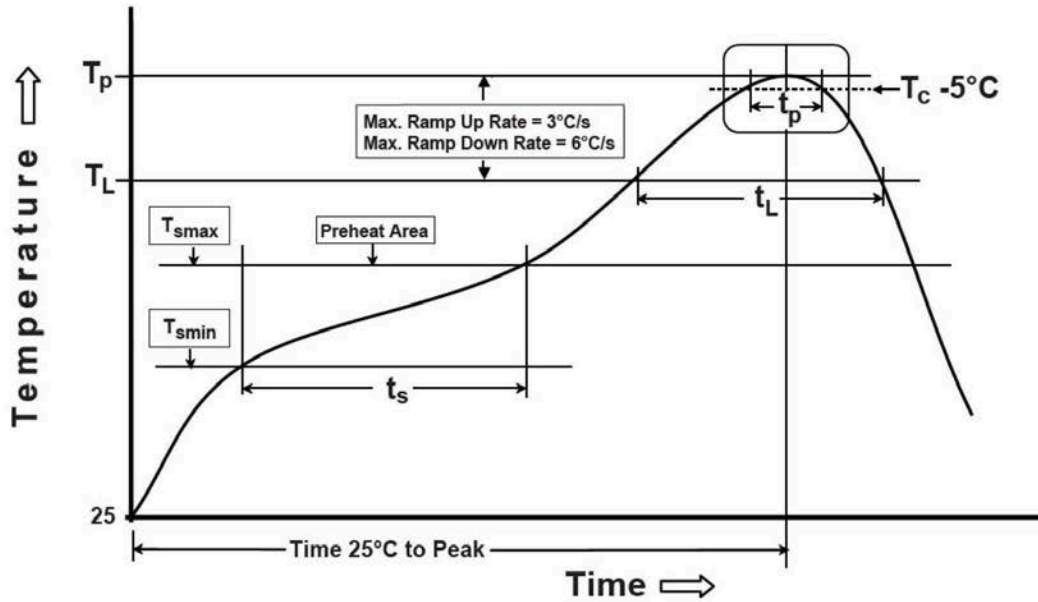


Dimensions: $\frac{\text{Inches}}{\text{mm}}$

Unless otherwise specified, all tolerances are: $\pm \frac{.010}{0,25}$



Non-Lead Recommended Reflow Profile (Based on J-STD-020D)



T_{SMIN} (°C)	T_{SMAX} (°C)	T_L (°C)	T_P (°C MAX)	t_s (s)	t_L (s)	t_p (s MAX)	Ramp-up rate (T_L to T_P)	Ramp-down rate (T_P to T_L)	Time 25°C to peak temperature (s MAX)
150	200	217	245	60-120	60-150	30	3°C/s MAX	6°C/s MAX	480

Notes:

1. All temperatures measured on the package leads.
2. Maximum times of reflow cycle: 2.

For More Information

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